

## WO02078101

Publication Title:

Method of creating an electroluminescent device

Abstract:

In a method of creating an electroluminescent device, a plurality of organic electroluminescent pixels (10) are created on a transparent substrate (7), active electronic circuitry (14) is created on a second separate substrate (15), and the two substrates are assembled together with the active electronic circuitry (14) facing the transparent substrate (7) in such a manner as to form discrete electrical connections (17) between the active electronic circuitry and the organic light emitting diode pixels (10). An insulating matrix material (18) can then be used to fill the space between the substrates (7, 15).

-----  
Data supplied from the esp@cenet database - <http://ep.espacenet.com>

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
3 October 2002 (03.10.2002)

PCT

(10) International Publication Number  
**WO 02/078101 A1**

(51) International Patent Classification<sup>7</sup>: **H01L 51/20**,  
21/56

(74) Agent: **HANSON, William, Bennett; JY & GW John-**  
son, Kingsbourne House, 229-231 High Holborn, London  
WC1V 7DP (GB).

(21) International Application Number: PCT/GB02/01372

(22) International Filing Date: 21 March 2002 (21.03.2002)

(81) Designated States (*national*): JP, US.

(25) Filing Language: English

(84) Designated States (*regional*): European patent (AT, BE,  
CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC,  
NL, PT, SE, TR).

(26) Publication Language: English

(30) Priority Data:  
0107236.2 22 March 2001 (22.03.2001) GB

**Declaration under Rule 4.17:**

— as to applicant's entitlement to apply for and be granted  
a patent (Rule 4.17(ii)) for the following designations JP,  
European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR,  
GB, GR, IE, IT, LU, MC, NL, PT, SE, TR)

(71) Applicant (*for all designated States except US*): **MI-**  
**CROEMISSIVE DISPLAYS LIMITED** [GB/GB];  
Scottish Microelectronics Centre, The King's Buildings,  
West Mains Road, Edinburgh EH9 3JF (GB).

**Published:**

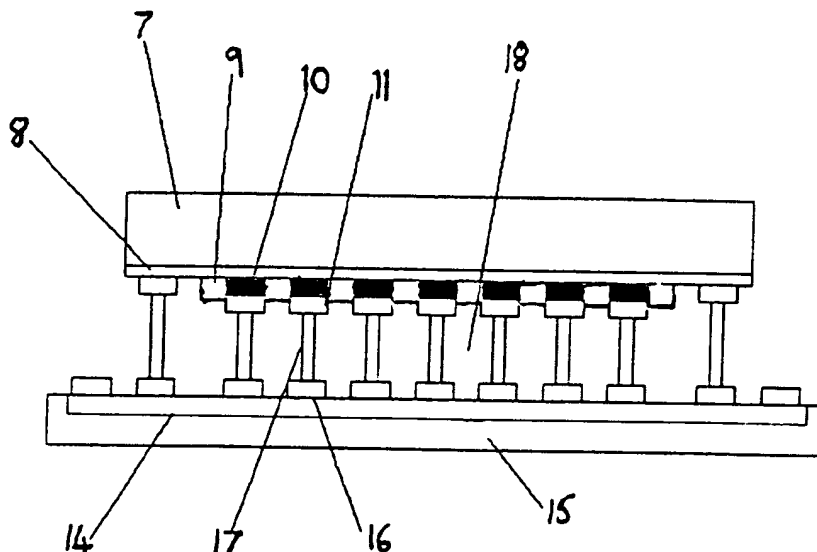
— with international search report

(72) Inventor; and

(75) Inventor/Applicant (*for US only*): **GOURLAY, James**  
[GB/GB]; 1 (2F2) Viewforth Square, Edinburgh EH10 4LP  
(GB).

*For two-letter codes and other abbreviations, refer to the "Guid-*  
*ance Notes on Codes and Abbreviations" appearing at the begin-*  
*ning of each regular issue of the PCT Gazette.*

(54) Title: METHOD OF CREATING AN ELECTROLUMINESCENT DEVICE



(57) Abstract: In a method of creating an electroluminescent device, a plurality of organic electroluminescent pixels (10) are created on a transparent substrate (7), active electronic circuitry (14) is created on a second separate substrate (15), and the two substrates are assembled together with the active electronic circuitry (14) facing the transparent substrate (7) in such a manner as to form discrete electrical connections (17) between the active electronic circuitry and the organic light emitting diode pixels (10). An insulating matrix material (18) can then be used to fill the space between the substrates (7, 15).

WO 02/078101 A1

## Method of Creating an Electroluminescent Device

### Background to the Invention

This invention relates to a method of creating an electroluminescent device and to a  
5 device created using the method.

The integration of optoelectronic and electronic components from different origins  
and substrates makes possible many advanced systems in diverse applications in  
photonics. To this end various hybrid integration methods and technologies have  
10 been developed.

Examples of heterogeneous integration are flip-chip bonding, epitaxial lift-off,  
substrate removal and appliqué bonding, and micro-robotic pick and place.

15 Heterogeneous integration can allow the manufacture of devices that combine  
component parts that have different properties (optical, mechanical, electrical, or  
thermal), realizing a product that has some combination of these properties. It can  
also be used as a way of combining distinct component parts, which require  
mutually exclusive manufacturing. Another advantage of this approach is to simplify  
20 the manufacture of, or improve the performance of, an end product, by removing the  
exposure of one component part to a manufacturing step that has a deleterious effect  
on the components performance in the end product.

This approach has been used to integrate silicon chips onto printed circuit boards.  
25 For example, a solder bumped flip chip process was introduced by International  
Business Machines Corporation in the early 1960's. This technology utilizes solder  
bumps deposited on wettable terminals on the chip and on the substrate. The solder-  
bumped chip is flipped and then aligned to the substrate and electrical connections  
are made by reflow of the solder. Similar flip chip techniques have been used to  
30 integrate gallium arsenide (GaAs) photodetector elements onto silicon microchips.

Organic electroluminescent devices have previously been manufactured that  
incorporate active matrix addressing schemes. Such devices have been demonstrated

- 2 -

using polysilicon TFTs (thin film transistors) and CMOS (complementary metal-oxide-semiconductor) crystalline silicon as the active addressing material. In all cases, this has involved the electroluminescent pixel structures being manufactured directly onto a substrate that houses the active matrix circuitry. This exposes the active matrix circuitry material to the processing steps required to build the pixel arrays, which can be disadvantageous.

For example, using the methods described in the prior art of the field, to manufacture an device with a CMOS active matrix addressing scheme involves a number of compromising processing steps. The fragile and sensitive CMOS substrate housing the active matrix driving circuitry is exposed to the potentially damaging environment, which is needed to manufacture the diode structures. The CMOS may also be exposed to (depending on whether small molecule, or polymer, emitting materials are used) a range of solvents, vacuum drying, and vacuum metallization processes, all of which can damage CMOS.

Conversely, by building the electroluminescent array onto the CMOS, the encapsulation of the device is compromised. This is because a transparent electrode structure must be processed on top of the light emitting material. This is routinely achieved by the deposition of a very thin layer (approximately 10 nm) of a certain metal, followed by the deposition of a thicker layer of transparent conducting material, typically indium tin oxide (ITO). The deposition of ITO exposes the underlying layers to both an oxygen rich and high temperature environment, both of which can reduce the lifetime of devices. This structure puts an oxygen rich compound (ITO), from which oxygen can migrate, directly next to either oxygen sensitive, metals or organic materials. The device requires further encapsulation using an impermeable barrier such as glass.

Organic light emitting materials are sensitive to water, oxygen, certain organic solvents, and temperatures much above 100 °C. Exposure to any of these can degrade the emissive properties, or cause catastrophic failure of a device. To build efficient diode structures it is also necessary to use other reactive materials in the

- 3 -

electrode structures, such as thin layers of calcium, magnesium, or other metals that oxidize readily. To protect these sensitive materials it is necessary to encapsulate the devices, ideally with barrier layers that are impermeable to water and oxygen.

- 5 The non-planar topology of the active circuit chip, on which the electroluminescent pixels must be formed, causes difficulties in encapsulation. The non-planar topology can also cause variations in device performance across a pixel, and possibly cause catastrophic effects such as short circuits between the top and bottom electrode.
- 10 When active circuitry is used in optical applications, it is usually necessary to include light-blocking layers as part of the manufacturing process. This is to avoid the light affecting the electronics, by causing charge leakage. Figure 1 shows, in schematic section, a conventional device in which light rays 1 are incident on top electrodes 2 as well as on light-blocking layers 3 between the top electrodes. An
- 15 underlying dielectric 4 covers an active circuit 5 which is arranged on an active circuit substrate 6.

#### Summary of the Invention

- I have invented a novel method of manufacturing active matrix displays. My
- 20 approach involves the heterogeneous integration of two components, one of which contains a pixel array and the other of which contains active matrix driving circuitry. The two are bonded together in such a fashion as to provide an electrically conductive pathway from one to the other so that the pixels can be electrically addressed by the circuitry.

- 25 The present invention thus provides a method of creating an electroluminescent device, comprising the steps of creating a plurality of organic electroluminescent pixels on a transparent substrate, creating active electronic circuitry on a second separate substrate, and assembling together the two substrates with the active
- 30 electronic circuitry facing the transparent substrate in such a manner as to form discrete electrical connections between the active electronic circuitry and the organic electroluminescent pixels.

- 4 -

This approach allows the optimization of manufacturing processes for each separate component. This method leads to several improvements in the performance of the final product over those that can be produced by current single manufacturing processes. The heterogeneous integration method also leads to a more efficient and economic manufacturing cycle.

The present heterogeneous integration method does not expose CMOS to any of the potentially damaging process steps mentioned above, and also enhances encapsulation by removing the need to deposit ITO (or other transparent conducting material) directly onto the upper surface of the electroluminescent device. This is because by dividing the manufacturing process into two distinct halves, it is possible to use a transparent and hermetic material, such as glass, that has been pre-coated with ITO or other transparent conducting material, as the substrate onto which the organic materials are deposited. It is also possible to create areas of insulating material that can act as electrically isolating barriers between pixels. This material is preferably light-absorbing or light-reflecting, so that together with the second electrode discussed below, it blocks light, and separate light-blocking layers are not required.

Each pixel may include one or more electroluminescent layers, each layer comprising a polymer, oligomer, small molecule, or rare-earth chelate material. These layers, and optionally other layers for enhancing performance such as a layer of conducting polymer for improved injection, are deposited by a spin-coating, dip-coating, spraying, doctor blading or vacuum deposition method, understood by those familiar with the field. A second electrode is then preferably vacuum deposited via a shadow mask on the organic layers. Typically, this consists of a thin layer of reactive low work function ( $<4$  eV) metal, or metal compound such as lithium fluoride. In a preferred subsequent step, prior to assembling the substrates, the second electrode is completed by encapsulating using a metallic material less reactive than said pads. The thin electrical contact pad layer may in particular be capped by depositing via a shadow mask a thicker layer of the less reactive metal such as aluminum, so that the reactive metal layer is fully encapsulated. A plurality

- 5 -

of emissive pixels is formed, each with an independently addressable second metal electrode contact.

5 In a preferred embodiment of the heterogeneous integration method, the active matrix and the pixel array are first aligned to one another using alignment structures on the active matrix substrate and on the electroluminescent pixel substrate. As the pixel substrate is transparent, this is not a flip chip procedure, so equipment costs are reduced. The active matrix substrate is then bonded to the pixel array so as to provide mechanical strength between the two components and an electrically  
10 conducting pathway between the active matrix circuitry and each emissive pixel, so that each pixel can be independently addressed.

The bonding can also involve a two-step process, whereby a plurality of electrical bonds is initially formed between the two components. This can give rise to a small  
15 gap spacing which can be filled by using a fluid non-conducting material, such as epoxy resin or other polymeric material, that can be converted into a rigid non-fluid, non-conducting matrix material. This material can be chosen to further improve the overall performance of the device. For instance, the non-conducting material when solidified may improve mechanical strength, thermal expansion mismatch, thermal  
20 management, electrical isolation/impedance or some combination of these to improve the reliability and lifetime of the devices. The matrix material may further enhance organic electroluminescent pixel array encapsulation by acting as a desiccant or getter.

25 The electrical connections between the substrates can be formed by a number of different methods. The connection material may comprise conductive adhesive as an alternative to solder or metal. Low-temperature curing conducting adhesive is particularly appropriate for the preferred method of applying the connection material: patterning by screen, stencil or ink-jet printing to form the electrical and  
30 mechanical connections between metal electrodes on the two substrates. This reduces the exposure of the organic materials to elevated temperatures and "wet" processes, and can allow for a plurality of connections to be made on one of the substrates at any one time during manufacture.

- 6 -

Other suitable bonding methods include the use of ultrasonic bonding to form the discrete electrical and mechanical connections between metal electrodes on the two substrates.

- 5 An alternative bonding method would involve the application of a material which has an anisotropy in its conductivity to only form the electrical connections between the metal electrodes on the two substrates. This material would be applied as continuous film between the two components, but would only be conductive between the corresponding metal electrodes on the two substrates.

10

This heterogeneous integration method can be used with a number of different substrates and provide improvements in performance of the finished electroluminescent device. The second substrate preferably comprises a material selected from crystalline silicon, amorphous silicon, polysilicon, silicon on insulator,  
15 or an electrically conducting organic material.

The inventive technique effectively isolates the optical and electrical parts of the device, so avoiding detrimental influences. The organic electroluminescent pixels act as an interface between the optical domain and the active electronic circuitry.

20

- By using this approach with a polysilicon, or amorphous silicon, active matrix circuitry device there is an improvement in the pixel fill factor. This is because, typically, using the methods known in the field, the polysilicon circuitry is visible in each pixel of the device. In the device according to the present invention, all the  
25 circuitry can be placed discretely behind the pixel, thereby increasing the active area of the pixel. Another advantage is that one can design and apply polysilicon circuitry that requires a larger physical size, as this no longer affects the pixel fill factor which influences the light output and perceived performance of the pixel(s) and device. Coarser physical circuit geometries can often be manufactured more  
30 economically providing cost reductions in the manufacturing cycle. The active circuitry is preferably shielded from exposure to any harmful light of particular wavelength emitted from, or striking, the device, and polysilicon is particularly appropriate in this context.



- 7 -

Different substrate materials such as plastics can be used to create devices which are curved, molded to a non-flat shape, or flexible.

5 This is particularly useful if the active electronic circuitry is created on a plurality of active circuit chips, which chips are combined with the second separate substrate. Methods that allow CMOS chips to be embedded in plastic substrates have recently been demonstrated. This has been achieved by fluidic self assembly of shaped CMOS silicon chip devices into predefined depressions in a plastic substrate followed by a metallization interconnect process.

10

By incorporating this approach with our heterogeneous integration method it is possible to increase the complexity of active circuitry embedded into a large area flexible electroluminescent device, as the circuitry would not be visible, being hidden behind the pixel structures.

15

The method advantageously comprises combining a plurality of transparent substrates with different properties, such as different color of emitted light, with a single second separate substrate.

20 The present invention also provides an electroluminescent device created by the method defined above. Finally, the invention provides an electroluminescent device comprising a plurality of organic electroluminescent pixels on a suitable transparent substrate, and active electronic circuitry on a second separate substrate, said transparent and active substrates being connected by discrete electrical connections  
25 between the active electronic circuitry and the organic light emitting diode pixels, a cured, non-fluid, non-conducting matrix material being located between the transparent and active substrates.

#### Brief Description of the Drawings

30 The present invention will now be described in more detail, by way of example only, with reference to the accompanying drawings, in which:

- 8 -

Figure 1 is a schematic vertical section through the known device comprising light-blocking layers discussed above;

Figure 2 is a schematic vertical section through a device according to a particular  
5 embodiment of the invention; and

Figure 3 is a more detailed vertical section through a single pixel of the device shown in Figure 2.

10 Detailed Description of the Preferred Embodiment

Figures 2 and 3 show a transparent substrate 7, of a material having good hermetic properties, such as glass. In a separate manufacturing process to that of an active circuit chip described below, the transparent substrate 7 is firstly coated with a transparent or semi-transparent electrode 8, preferably comprising ITO.

15

Walls 9 of light-absorbing, electrically insulating material are constructed on the transparent electrode 8 to isolate the pixels electrically and optically. The walls define an array of spaces in each of which an electroluminescent pixel 10 is manufactured on the electrode 8, by a spin-coating, dip-coating, spraying, doctor  
20 blading or vacuum deposition method known in the field. The pixel 10 comprises one or more organic electroluminescent layers, each layer comprising a polymer, oligomer, small molecule, or rare earth chelate material. Additionally, the pixel 10 may include one or more inorganic electroluminescent layers, conducting layers and/or barrier layers as known in the art.

25

A second electrode 11 is then vacuum deposited via a shadow mask on each organic pixel 10. This second electrode consists of a thin layer 12 of reactive low work function ( $<4$  eV) metal, or metal compound such as lithium fluoride. The layer 12 is capped by depositing via a shadow mask a thicker layer 13 of less reactive metal  
30 such as aluminum, so that the reactive metal layer 12 is fully encapsulated. A plurality of emissive pixels 10 is formed, each with an independently addressable second metal electrode contact 11.

- 9 -

Active matrix addressing scheme circuitry 14 is separately manufactured by methods known to practitioners in the field, such as CMOS techniques, on a suitable substrate 15. The active matrix pixel circuitry includes metal electrode contact pads 16. The electrode pads may be the only electrically conducting parts of the circuitry  
5 that are not protected by insulating layers.

A conducting material, such as a low-temperature curing conducting adhesive, is applied to either or both of, on the one hand, electrical bumps 11a of the second electrodes 11 and on the other hand, the contact pads 16, in such a way that when  
10 the components come into contact only a discrete electrical connection 17 is made between the respective contacts of the two substrates 7, 15. When using certain conducting materials to form the electrical connections it may be advantageous to apply the conducting material to both sets of electrical contacts.

15 Having allowed the electrical contacts to become thermodynamically stable and mechanically strong, it is then possible to fill the space between the discrete electrical contact points with an initially fluid, electrically insulating material 18, such as epoxy resin. This has the effect of further electrically isolating the individual contact points. By judicious prudent choice of this insulating matrix material 18 it is  
20 also possible impart further desirable properties to the active matrix organic electroluminescent device, such as improved compliance, thermal expansion matching, mechanical strength, ruggedness, or other desirable properties for improved reliability, yield and lifetime. For example, glass and silicon have different thermal expansion coefficients, and the matrix material 18 can ameliorate the  
25 problem of unequal expansion of the substrates 7, 15 by acting as a compliant layer. Also, during heat cycling of the assembled device, the matrix material 18 can assist in absorbing mechanical stresses in the electrical connections 17 between the organic electroluminescent pixels 10 and the active circuitry 14. Additionally, gettering and desiccant materials such as barium, zeolites, zirconium and/or silica  
30 can be included in the matrix material 18, to reduce leakage of environmental gases and liquids into the pixel area, and thereby improve the device lifetime.

- 10 -

All forms of the verb "to comprise" in this specification mean "to consist of or include".

- 11 -

CLAIMS

1. A method of creating an electroluminescent device, comprising the steps of:
  - 5 creating a plurality of organic electroluminescent pixels on a transparent substrate;  
creating active electronic circuitry on a second separate substrate; and  
assembling together the two substrates with the active electronic circuitry facing the  
10 transparent substrate in such a manner as to form discrete electrical connections  
between the active electronic circuitry and the organic electroluminescent pixels.
2. A method according to claim 1 wherein said second substrate comprises a  
material selected from crystalline silicon, amorphous silicon, polysilicon, silicon on  
15 insulator, or an electrically conducting organic material.
3. A method according to claim 1 or 2 wherein said transparent substrate is  
coated with an electrically conducting transparent material.
- 20 4. A method according to claim 3 wherein the transparent conducting material  
comprises indium tin oxide.
5. A method according to any preceding claim, comprising creating areas of  
insulating material providing electrically isolating barriers between pixels.  
25
6. A method according to claim 5, wherein said insulating material is light-  
absorbing or light-reflecting.
7. A method according to any preceding claim wherein emissive material used  
30 in the organic electroluminescent pixels is selected from a polymer, oligomer, small  
molecule, or rare earth chelate material.

- 12 -

8. A method according to claim 1 and 2, wherein a second electrode of each pixel comprises a thinner layer of a reactive metal or metal compound, arranged between the electroluminescent material of the pixel and a thicker layer of a metallic material less reactive than said reactive metal or compound.
- 5
9. A method according to any preceding claim wherein the electrical connections between the substrates comprise a connection material selected from conductive adhesive, metal and anisotropic conductive material.
- 10
10. A method according to claim 9 wherein the electrical connection material is deposited onto contact pads of one or both substrates by a method selected from screen-printing, stencil-printing and ink-jet printing.
11. A method according to claim 9 or 10 wherein a plurality of connections on at least one of the substrates is formed by depositing the connection material in a plurality of locations simultaneously on the respective substrate.
- 15
12. A method according to any preceding claim wherein the substrates are connected together by ultrasonic bonding.
- 20
13. A method according to any preceding claim wherein a fluid insulating material is introduced and fills the gaps between the electrical connections between the two substrates and is then converted into a solid matrix.
- 25
14. A method according to claim 13 wherein the matrix comprises a material which improves the thermal expansion properties between different materials in the device.
15. A method according to claim 13 or 14 wherein the matrix further comprises a desiccant or getter.
- 30
16. A method according to any preceding claim wherein one or both of the substrates is of plastics material.

- 13 -

17. A method according to any preceding claim wherein one or both of the substrates is flexible.

18. A method according to any preceding claim wherein one or both of the  
5 substrates is non-planar.

19. A method according to any preceding claim wherein the active electronic circuitry is created on a plurality of active circuit chips, which chips are combined with the second separate substrate.

10

20. A method according to any preceding claim wherein a plurality of transparent substrates with different properties, such as different color of emitted light, are combined with a single second separate substrate.

15 21. An electroluminescent device created by a method according to any one of the preceding claims.

22. An electroluminescent device comprising a plurality of organic electroluminescent pixels on a suitable transparent substrate, and active electronic  
20 circuitry on a second separate substrate, said transparent and active substrates being connected by discrete electrical connections between the active electronic circuitry and the organic light emitting diode pixels, a cured, non-fluid, non-conducting matrix material being located between the transparent and active substrates.

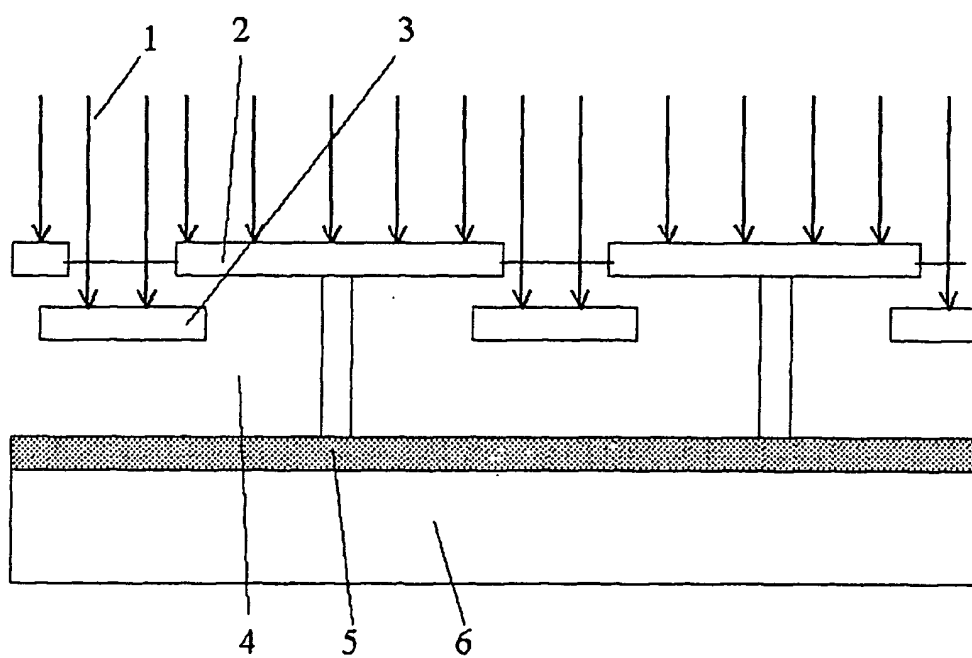
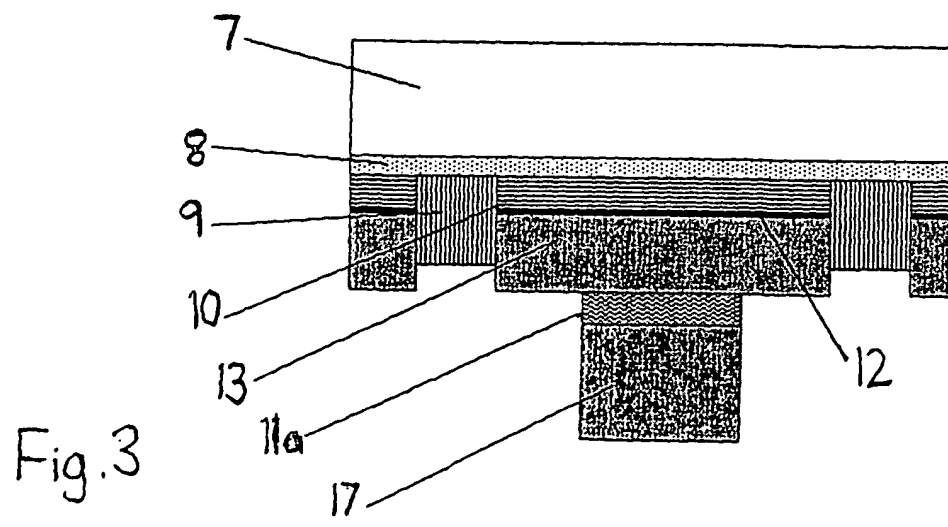
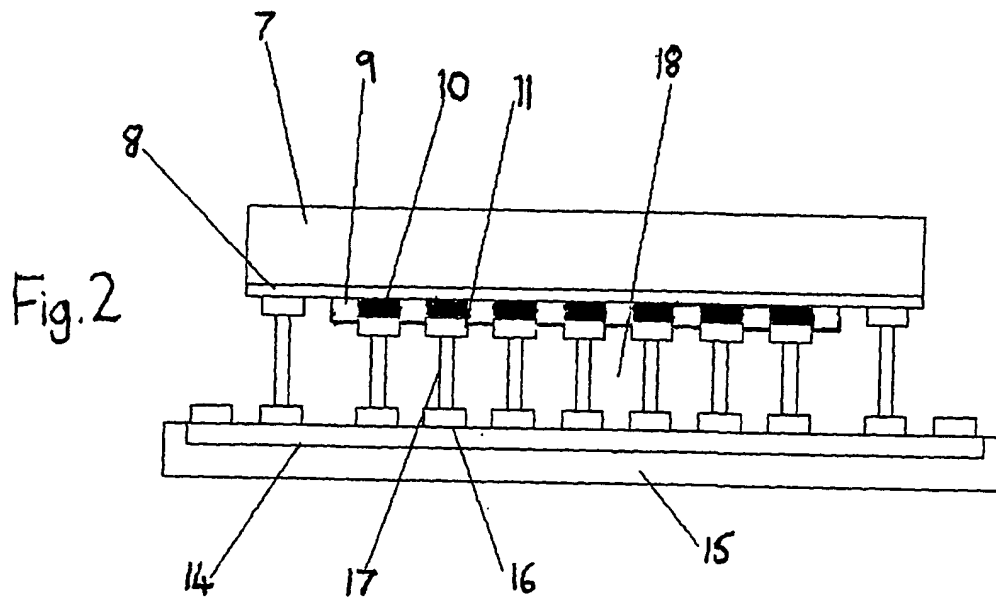


Fig. 1 (Prior Art)





## INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 02/01372

A. CLASSIFICATION OF SUBJECT MATTER  
 IPC 7 H01L51/20 H01L21/56

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

PAJ, EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 2000, no. 06, 22 September 2000 (2000-09-22) -& JP 2000 082720 A (CANON INC), 21 March 2000 (2000-03-21)	1-12, 16-21
Y	abstract	8,13-15, 22
Y	EP 0 982 783 A (TDK CORP) 1 March 2000 (2000-03-01)	8
A	page 6, line 44 -page 7, line 58; figure 1 page 10, line 45 -page 11, line 20	6,20
Y	EP 0 878 839 A (MATSUSHITA ELECTRIC IND CO LTD) 18 November 1998 (1998-11-18) page 8, line 18 -page 9, line 14; figure 1	13,14,22
	--- -/--	

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

## \* Special categories of cited documents :

\*A\* document defining the general state of the art which is not considered to be of particular relevance

\*E\* earlier document but published on or after the international filing date

\*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

\*O\* document referring to an oral disclosure, use, exhibition or other means

\*P\* document published prior to the international filing date but later than the priority date claimed

\*T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

\*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

\*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

\*&\* document member of the same patent family

Date of the actual completion of the international search

4 July 2002

Date of mailing of the international search report

11/07/2002

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
 NL - 2280 HV Rijswijk  
 Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
 Fax: (+31-70) 340-3016

Authorized officer

Edmeades, M

## INTERNATIONAL SEARCH REPORT

Inte al Application No

PCT/GB 02/01372

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	WO 01 05203 A (SUNSTAR ENGINEERING INC ;OKUNO TATSUYA (JP); GOTOH JOHSHI (JP)) 18 January 2001 (2001-01-18) page 4, line 22 -page 5, line 14; figure 1 ---	15
A	US 5 060 027 A (GOODING JOHN ET AL) 22 October 1991 (1991-10-22) figure 2 -----	1,21

## INTERNATIONAL SEARCH REPORT

Inter. Application No

PCT/GB 02/01372

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
JP 2000082720	A	21-03-2000	NONE	
EP 0982783	A	01-03-2000	EP 0982783 A2	01-03-2000
			JP 2000133447 A	12-05-2000
			US 6339290 B1	15-01-2002
EP 0878839	A	18-11-1998	EP 1154470 A2	14-11-2001
			EP 0878839 A1	18-11-1998
			EP 0724289 A2	31-07-1996
			JP 3093621 B2	03-10-2000
			JP 9064103 A	07-03-1997
			JP 2001035889 A	09-02-2001
			KR 181615 B1	15-04-1999
			US 5641996 A	24-06-1997
			AU 695142 B2	06-08-1998
			AU 6015496 A	09-01-1997
			CA 2221286 A1	27-12-1996
			CN 1185231 A	17-06-1998
			FI 974488 A	09-02-1998
			WO 9642106 A1	27-12-1996
			NO 975833 A	03-02-1998
			SE 9704602 A	05-02-1998
WO 0105203	A	18-01-2001	BR 0012273 A	02-04-2002
			EP 1195083 A1	10-04-2002
			WO 0105203 A1	18-01-2001
			NO 20020060 A	22-02-2002
US 5060027	A	22-10-1991	GB 2249428 A	06-05-1992
			EP 0383889 A1	29-08-1990
			WO 9001803 A1	22-02-1990